

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Jon M. Huppenthal, Thomas R. Seeman, Lee A. Burton
Serial No. 09/932,330
Filed: August 17, 2001
For: SWITCH/NETWORK ADAPTER PORT FOR CLUSTERED
COMPUTERS EMPLOYING A CHAIN OF MULTI-ADAPTIVE
PROCESSORS IN A DUAL IN-LINE MEMORY MODULE FORMAT

Confirmation No.: 4801
Examiner: Sorrell, Eron J.
Art Unit: 2182

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT BASED ON AN
INTERNATIONAL SEARCH REPORT AND PETITION REQUESTING CONSIDERATION

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR 1.97(d), Applicant hereby petitions for consideration of this Information Disclosure Statement (IDS) and the references on the enclosed Form PTO/SB/08A. In submitting these references, no representation is made or implied that the references are or are not material to the examination of the application. The Examiner is encouraged to make his or her own determination of materiality. Copies of the non-patent references are provided.

This IDS is filed pursuant to 37 CFR 1.97(d), after issuance of a Notice of Allowance but before payment of the Issue Fee. Pursuant to 37 CFR 1.97(e), it is hereby certified that each item in this IDS was cited in a communication from a foreign patent office in counterpart Japanese Appl. No. 539441/2000, dated 8 January 2008, a date not more than 3 months prior to the filing of this statement.

Please charge Deposit Account No. 50-1123 the \$180 petition fee and any fee deficiency associated with this submittal.

Date: 21 March 2008

Respectfully submitted,



William J. Kubida, Reg. No. 29,664
HOGAN & HARTSON
One Tabor Center
1200 17th Street, Suite 1500
Denver, Colorado 80202
(719) 448-5909 Tel
(303) 899-7333 Fax